Ph.D Supervision

 **2016**	**Pittala Chandra Sekhar**, "SOME NEW OPERATIONAL TRANS-RESISTANCE AMPLIFIER BASED SINUSOIDAL / SQUARE WAVEFORM GENERATORS," **Vignan's Foundation for Science, Technology and Research** *(Deemed to be University),* Vadlamudi-522213, Guntur, A.P, India, **October 2016.**
 **2017**	**Vallabhuni Vijay**, "SECOND GENERATION DIFFERENTIAL CURRENT CONVEYOR (DCCII) AND ITS APPLICATIONS", **Vignan's Foundation for Science, Technology and Research** *(Deemed to be University),* Vadlamudi-522213, Guntur, A.P, India, **July 2017.**
 **2018**	**Musala Sarada**, "DESIGN AND DEVELOPMENT OF FULL SWING, HIGH SPEED EX-OR AND EX-NOR LOGIC CIRCUITS USING MOSFETS / FinFETS /CNTFETs", **Vignan's Foundation for Science, Technology and Research** *(Deemed to be University),* Vadlamudi-522213, Guntur, A.P, India, **April 2018.**
 **2019**	**Jithendra Kumar Saini,** "SOME NEW LOW-POWER HIGH-SPEED FULL ADDER CIRCUITS AND THEIR APPLICATIONS USING CARBON NANOTUBE FIELD EFFECT TRANSISTOR*"*, **Manipal University,** Jaipur-302034, Rajasthan, India, **October 2019.**
 **2020**	**Syed Zahiruddin**, "DESIGN OF WAVEFORM GENERATORS, OSCILLATORS AND MODULATORS BY USING CCCII", **Vignan's Foundation for Science, Technology and Research** *(Deemed to be University),* Vadlamudi-522213, Guntur, A.P, India, **June 2020.**
 **2021**	**Petluri Venkata Subbarama Murali Krishna,** "STUDIES OF SECOND-GENERATION CURRENT CONVEYOR (CCII) BASED NOVEL ALL-PASS FILTERS AND OSCILLATORS", **Birla Institute of Technology**, Mesra-835215, Ranchi, India, **April 2021.**
	**Posani Vijaya Lakshmi,** "DESIGN OF LOW POWER ANALOG FRONT END INTENDED FOR ECG SIGNAL ACQUISITION SYSTEM", **Vignan's Foundation for Science, Technology and Research** *(Deemed to be University),* Vadlamudi-522213, Guntur, A.P, India, *(Rough draft copy of the PhD Synopsis & Thesis submitted on 09-04-2021).*

Ongoing

1. **Kancharla Vijaya Vardhan,** "ANALYSIS AND DESIGN OF HIGH SPEED RESIDUE NUMBER SYSTEMS AND ITS APPLICATIONS USING NC LAUNCH - ENCOUNTER TOOL IN CADENCE", PhD degree thesis work, **Vignan's Foundation for Science, Technology and Research** *(Deemed to be University),* Vadlamudi, Guntur, A.P, India.
2. **Rithambara,** "SOME NEW CCCII/CDTA-BASED APPLICATIONS USING MOSFETs/CNTFETs/FinFETs", PhD degree thesis work, **JECRC University,** Jaipur, India.
3. **Anju Rajput,** "SOME NEW HIGH FREQUENCY TUNABLE OSCILLATORS USING CURRENT MODE DEVICES", PhD degree thesis work, **Manipal University,** Jaipur, India.
4. **Muttineni Suman,** "DESIGN OF FULL SWING DIRECT THREE INPUT EX-OR/EX-NOR LOGIC CIRCUITS USING CNTFETs/FinFETs", PhD degree thesis work, **JECRC University,** Jaipur, India.